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	Filing Date		2003-07-23	
	First Named Inventor	Paul James Tayson		
	Art Unit	2168		
	Examiner Name	Cheyne D. Ly		
Attorney Docket Number		6647-049		

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1	Lin et al., On Transformation of Logic Specifications into Procedural Programs, IEEE, pages 274-281.	<input type="checkbox"/>
2	Shieh et al., Fine Grain Scheduler for Shared-Memory Multiprocessor Systems, IEEE, pages 98-106.	<input type="checkbox"/>

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